

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,158,551 B2
APPLICATION NO. : 10/015616
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INVENTOR(S) : Yuji Tochio

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

Column 2 (Abstract), change the entire Abstract to --A driver circuit for a semiconductor laser includes a first bias current supply section supplying, at least at a time of non-output of data, a first bias current to an LD. A signal processing section generates a pulse current control signal in which a burst data signal is delayed, and generates a second bias current control signal which rises more rapidly by a predetermined time than the rise of burst data included in the pulse current control signal. A pulse current supply section supplies to the LD a pulse current generated in accordance with the pulse current control signal. A second bias current supply section supplies to the LD a second bias current generated in accordance with the second bias current control signal.--.

Signed and Sealed this

Eighteenth Day of March, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office